





Device Service Certificate of Results

<u>Customer Information</u>						
Customer Name:			ASAP Semiconductor			
Customer Contac	t:		Joe Faruqui			
Customer Address	:		3625 W MacArthur Blvd			
			Suite 314			
			Santa Ana, CA 92704			
Device Information						
Device:			3550S-1-502			
Manufacturer:			Bourns			
D/C:			8721			
Received Quantity	' :		5			
Test Quantity:			5			
Test Facility Locati	on:		Aus1			
Sales Support:			Griffin Jarzombek			
Results Summary						
Visual Results			Electrical Results			
RtS per MIL-STD-883G, Per Method 2015			Per Quoted Parameters			
RtS Testing(From Sampling): Pass			Temp:		25c	
Leads/Balls(From Sampling): Pass						
Body(From Sampling): Pass			QTY Pass: 5			
1			QTY Fail:	0		
Markings(From Sampling): Pass			,			
Decapsulation:	Not performed					
Solderability Testing			X-Ray Testing			
MIL-STD-883G, Per Method 2003			MIL-STD-883G, Per Method 2010			
QTY Tested: Not Performed		QTY Tested:		erformed		
QTY Pass: Not Performed		Notes: Not Performed				
QTY Fail: Not Performed						
<u>Findings:</u> Based on the results of processing performed no non-conformities were found.						
Processes Performed (Checked items listed below):						
☑ QA Incoming	☑ Complete Lot	Decapsulation	☐ X-Ray	Solderability	☐ C-SAM	
Sample Visual	Visual		,	Testing		
☑ Electrical Testing	☐ Electrical Testing	Solder Dipping	Bake and Dry	☐ Tape and Reel	Burn In	
at Ambient	at Temperature		Pack	_ '		
☐ Device	Other	☐ Other	Other	☐ Other	☑ QA Outgoing	
Programming					Validation	

Device Service Results Form GD4 Test Services, Inc. GD4-Form-MM-002.2 Customer Part Number: 3550S-1-502

Customer PO Number: Pre Pay; Quote Number: 11558

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Tracker ID: 13-122711-006 Lot Completion Date: 01/06/2012







Incoming/ Receiving:

(Description and images of lot and units as received).

Date Received: 01/04/2012

Quantity Expected: 5
Quantity Received: 5

Reason for lot count deviation:

D/C Counts: 8721

Received by: LP
Package Type: tin can
Product Transport Type: Trays

Incoming Notes (packaging issues, damaged parts, etc.):
Sample Incoming Photos:









Incoming Visual Test Sample Validation

(Random inspection of 2 units or other quantity as outlined in quote/PO. Inspection includes: validation of body, markings, balls/leads, & RtS Testing).

Date of Process Step: 01/06/2012
Quantity for Test: 5

Body/Leads Sample Validation:

Quantity Pass: 5
Quantity Fail: 0

Device Markings (Verify part number, manufacturer, and Date Code; Per PO):

Pass
Device Body (Examine for chips, scratches, and other defects):

Pass
Device Leads/Balls (Examine for excessive bent leads and evidence of rework):

Pass

Resistance to Solvents Sample Validation:

Resistance to Solvents (RTS) per MIL-STD-883G, Per Method 2015(sample from lot): Pass RtS at 25c:

Pass RtS at 65c:

Pass

Visual Test Notes:

RtS Sample photos:

RtS at 25C:

Before After

Unit 1, D/C:



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RtS at 65C:

Before

After

Unit 1, D/C:



Sample photos from visual, if applicable:



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Lot Visual Validation

(Visual inspection of each unit within the lot, including validation of bodies, markings, & balls/leads, evidence of prior use, damage, bent/broken ball/leads, & other gross physical non-conformities).

Date of Process Step:	01/06/2012
Quantity for Test:	5
Body/Leads Validation:	
Quantity Pass:	5
Quantity Fail:	0
Break down of visual failures found:	
Device Markings (Examine for fading, residue, or missing markings):	0
Device Body (Examine for chips, scratches, and other defects):	0
Device Leads/Balls (Examine for excessive bent leads and evidence of rework):	0
Visual Notes:	
Sample photos from Visual if applicable:	







Electrical Test Validation

(Functional Electrical testing to quoted test parameters). Date of Process Step: 01/06/2012 Quantity for Test: 5 **Quantity Pass:** 5 Quantity Fail: 0 **Quantity Lost:** 0 **Quantity Damaged:** 0 Test Temperatures: 25c Test Equipment Utilized ETS LSi5 **Test Parameters:** Functionality Rated resistance Test Parameters/Limits: Functionality: P/F

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Rated resistance: P/F

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